

**VESTIGIAL SIDEBAND RECEIVER AND
METHOD FOR RESTORING CARRIER WAVE**

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

10 The present invention relates to a digital television,
and more particularly to, a vestigial sideband receiver for
receiving signals which are transmitted by being modulated in
a vestigial sideband and a method for restoring carrier waves
by receiving the signals.

15 2. Description of the Related Art

20 In general, vestigial sideband system of Grand Alliance
which is adopted as a standard of a transmission system of
digital televisions such as HDTVs in the united states and in
Korea, modulates a sideband signal of two sidebands generated
upper and lower parts with relation to a carrier wave when
amplitude-modulating a signal and the other one sideband
signal is largely reduced. That is, a sideband spectrum of a
baseband is removed to a pass band to transmit for utilizing a
band area effectively.

25 If a DC spectrum of a base band is removed to a pass band
in case of the VSB modulation, the DC spectrum is converted to
a tone spectrum, of which a signal is a so-called pilot
signal. That is, when a broadcasting bureau performs the VSB
modulation, the pilot signal is carried to transmit via air
for precisely demodulating the signal in a receiver.

Fig. 1 is a schematic block diagram of a transmission system of a general digital TV, in which a randomiser 101 outputs an input data randomly to a read-Solomon R-S encoder 102 for generating a white symbol, the R-S encoder 102 performs R-S coding of the randomly input data for inner and outer channel coding of the signal and adds a parity code of 20 bytes to output to a data interleaver 103.

The data interleaver 103 performs interleaving of the R-S coded data according to some preset regulations to output to a trellis encoder 104, and the trellis encoder 104 converts the interleaved data to a symbol in byte for trellis coding and outputs to a multiplexor 105. The multiplexor 105 performs multiplexing of a segment synchronising signal and a filed synchronising signal to a trellis coded symbol row per segment and frame to form a frame, and outputs the frame to a pilot insertion part 106. The pilot insertion part 106 inserts a pilot signal of a DC value to the framed transmission symbol to output to a VSB modulation part 107. The VSB modulation part 107 modulates the symbol row which is inserted with the pilot signal in the VSB system to output to a RF up-converter 108, and the RF up-converter 108 converts the VSB signal of the converted base band to a RF pass band signal for effectively transmit the signal via an antenna and transmits the signal via the antenna.

Fig. 2 shows a spectrum of a VSB transmission signal, in which Fig. 2(a) shows a spectrum of a base band which is

inserted with the pilot signal, and Fig. 2(b) shows a spectrum of a pass band after change into a transmission signal of the VSB system. As shown in Fig. 2(b), in the spectrum of the pass band, only a sideband spectrum of the base band exists, in which the pilot signal of a sideband exists.

In the meantime, if a digital data is VSB modulated as above and transmitted via air with an antenna from a broadcasting bureau, a domestic digital TV receiver should receive and demodulate the data to watch.

Fig. 3 is a schematic block diagram showing the digital TV receiver, in which a pass band signal of a particular channel is extracted via a tuner, a carrier wave is restored by using a pilot signal inserted in the sideband and a symbol timing is restored and a channel compensation is performed from the restored base band signal to extract a transmission symbol.

That is, if a RF signal which is modulated in the VSB system is received via an antenna, after a tuner 301 selects a desired channel frequency by using a heterodyne modulation system, the VSB signal of the RF band carried on the channel frequency is lowered to a fixed intermediate frequency IF (in general, 44MHz or 43.75MHz is widely used) and signals of other channels are properly filtered.

An output signal of the tuner 301 passes through a surface acoustic wave SAW filter 301, which is employed for removing signals of other bands and noise signals and for

analogue matching.

As an example, the digital broadcasting signal has all information in a band in the intermediate frequency from 44MHz to 6MHz, so that the SAW filter 301 removes all sections except a band of 6MHz, in which information from the tuner 301 exists, and outputs the band to a demodulation and FPLL part 303.

The demodulation and FPLL part 303 demodulates signals I and Q of the base band from the output and locks its frequency and phase.

That is, if an output of VCO 313, of which a central frequency is fixed to the intermediate frequency, for example, 46.69MHz, is input to a second multiplier 309 to be multiplied with an output from the SAW filter 302, a channel signal of Q of the base band is demodulated. Further, if the output from the VCO 313 is input to a first multiplier 304 after delaying its phase by 90 degrees in a phase shifter 308, and multiplied with the output from the SAW filter 302, a channel signal I of the base band is demodulated.

In the meantime, a pilot frequency, which is inserted in a broadcasting bureau, should exist precisely in the intermediate frequency for example 46.69MHz for normal operation in other receiving parts but usually not in the 46.69MHz. The output frequency of VCO 303 is, however, fixed to 46.69MHz, a bite may exist as much as a frequency difference output from the first and second multipliers 304

and 309 if the pilot output frequency is not 46.69MHz in the SAW filter 302.

The FPLL is utilised to remove the beat frequency by changing the frequency and phase of the carrier wave by changing an oscillation frequency of the VCO 313. Accordingly, an object of the FPLL is to find out a direction and a size of the movement of the oscillation frequency of the VCO 313, in which an automatic frequency control AFC loop filter 306, a limiter 307, a third multiplier 308 and an automatic phase control APC loop filter 312 are utilised and called as the FPLL.

Operations of the FPLL will be described as follows.

If a frequency of a signal I of a base band which is low-pass filtered in a low pass filter 305 after being demodulated in the first multiplier 304 is ω_0 , a pilot output frequency of the SAW filter 302 is ω_i , $\cos(\omega_i - \omega_0)t = \cos \Delta\omega t$, wherein $\Delta\omega = \omega_0 - \omega_i$ (beat frequency).

In the meantime, a signal Q of a base band which is low-pass filtered in a low pass filter 310 after being demodulated in the second multiplier 309, has a shape of $\sin \Delta\omega t$.

At this time, the AFC loop filter 306 is composed with a secondary manual filter capable of locking the beat frequency and outputs phase values with relation to each beat frequency of the signal. An output of the AFC loop filter 306 is input

to the limiter 307 to be amplified and limited.

An output from the limiter 307 is multiplied with the signal Q in the third multiplier 311. An output from the multiplier 311 passes through the APC loop filter 312, which
5 limits a band of a signal to 2KHz to control the VCO 313.

That is, FLL process is performed in case that the beat frequency exists and an output from the limiter 307 varies. When the FLL is finished and the output of the limiter 307 does not varies any more, then PLL process for correcting a phase is carried out.

The signal I which is demodulated to a base band in the demodulation and FPLL part 303, is converted into a digital signal and an A/D converter 314, and output to an I channel processing part 315, wherein the signal Q is utilised for restoring the carrier wave.

The I channel processing part 315 is composed with a synchronising signal abstractor, a channel compensator, an error extractor and the like, wherein the process as shown in the transmission system of Fig. 1 is performed reversely. That
20 is, a data segment synchronising signal, a field synchronising signal and the like, which are inserted in the transmission, are restored from the digitalized signal I and a received signal, that is, a transmission symbol is restored by using the synchronising signals.

25 If there is a frequency offset in the IF signal which is output from the tuner 301, then a spectrum of the received

signal is cut away by the SAW filter 302.

The frequency offset in case of a positive direction and a negative direction is as follows.

First, if the frequency offset in the positive direction as shown in Fig. 4(a), a right sideband is cut away and a pilot signal positioned in a left sideband revives as it is. Therefore, a cut away received signal component may be compensated in an equaliser (not shown) of the I channel processing part 315 in some degree so that no problems may occur in the performance of the system.

In the frequency offset in the negative direction as shown in Fig. 4(b), however, a left sideband is cut away by the SAW filter 302 so that a signal component as well as the pilot component disappear. In this case, the restoring of the carrier wave may not be properly carried out in the demodulation and FPLL part 303. Even though the carrier wave is restored due to a little remaining pilot component, phase jitters is too large in the received signal of the base band, degrading the performance of a receiver largely.

In fact, in case that an analogue FPLL is employed for restoring the carrier wave, as shown in Fig. 3, an acquisition range of the carrier wave is asymmetrical in $-110\text{KHz} \sim +200\text{KHz}$, which is resulting from the sideband signal component removing of the SAW filter 302.

Such a problem cannot be solved by changing the SAW filter with an element having a wider pass band for removing

noise or other channel signals instead of using the SAW filter as a matching filter, or changing the demodulation and carrier wave restoring process into a digital system.

Fig. 5 is a schematic block diagram for showing a structure of a VSB receiver in a digital system. In Fig. 5, as a RF signal, which is modulated in the VSB system, is received via an antenna, a tuner 501 selects a desired channel frequency and a VSB signal of a RF band carried on the channel frequency is lowered to a fixed intermediate frequency band, in which usually 44MHz or 43.75MHz is widely used, and output to an A/D converter 503 via a SAW filter 502. The A/D converter 503 digitalizes an output from the SAW filter 502 with the fixed frequency and outputs it to a re-sampling part 504 for conversion into a symbol-restored signal. The re-sampling part 504 is provided with timing errors between the digitalized signals and performs interpolation in a direction for reducing the errors. An output from the re-sampling part 504 is output to a digital matching filter 506 which has a fixed coefficient, wherein the SAW filter 502 has no matching function, so that the matching filter 506 is used for producing a maximum SNR at receiving ends.

Accordingly, it has a still problem that the pilot signal is cut away in the digital matching filter 505 even though an offset exists and the SAW filter 502 passes the pilot signal as it is.

There are many suggestions to resolve this problem.

First, the coefficient of the matching filter is not to be fixed but to be a variable. That is, information as to whether a current IF signal received from the carrier wave restoring system has how many frequency offsets, is reflected into the variable of the matching filter to transmit the pilot signal to a digital FPLL 510 without losing it. This proposal has, however, disadvantages that the carrier wave restoring system needs a lot of variable multipliers, complicating the hardware, and that the process for obtaining the frequency offset of the current IF signal from the carrier wave restoring system is also complicated. Further, the offset becomes too large in an initial stage, a normal operation cannot be performed if the carrier wave restoring is not achieved.

There is another proposal to put the digital matching filter in the base band. This proposal can smoothly restore the carrier wave since the pilot signal can be transmitted to the base band. However, this proposal has a still problem that a spectrum of a base band of a received signal is suddenly and sharply increased in the vicinity of a DC, resulting in a large loss of the SNR in spite of the compensation of the I channel processing part 509. Accordingly, since it is not proper to have the SNR loss in order to make the carrier wave restoring system solidier, this proposal is not preferable.

SUMMARY OF THE INVENTION

An object of the invention is to solve at least the above problems and/or disadvantages and to provide at least the advantages described hereinafter

5 Another object of the present invention is to provide a VSB receiver and a carrier wave restoring method in which a pilot signal is extracted from a pass band for restoring a carrier wave, thereby performing a stable restoring of the carrier wave.

A further object of the present invention is to provide a VSB receiver and a carrier wave restoring method, in which carrier waves may be symmetrically restored without any degradation of performance even though a frequency offset of a VSB signal in a RF or IF band is large in a positive or negative direction.

Still another object of the present invention is to provide a VSB receiver in which carrier waves are restored in a whole digital system and hardware thereof is simplified.

Additional advantages, objects, and features of the
20 invention will be set forth in part in the description which follows and in part will become apparent to those having ordinary skill in the art upon examination of the following or may be learned from practice of the invention. The objects and advantages of the invention may be realised and attained
25 as particularly pointed out in the appended claims.

To achieve the objects and in accordance with the

purposes of the invention, as embodied and broadly described herein, a VSB receiver includes a digital processing part for selecting a desired channel frequency via an antenna and converting the desired channel frequency to an intermediate
5 frequency to digitalize a predetermined band of the intermediate frequency, a carrier wave restoring part for extracting pilot components from a signal of the digitalized pass band to restore carrier waves, a demodulator for separating components I and Q from the signal of the digitalized pass band and multiplying the components with a complex carrier wave which is restored in the carrier wave restoring part to demultiplex the components to signals I and Q of a base band, and a symbol restoring part for restoring a transmission symbol from the signal I of the demodulated base band.

The digital processing part includes a surface acoustic wave SAW filter for passing the predetermined band of the middle frequency, a digital converter for digitalizing signals which are passed through the SAW filter, a digital matching
20 filter for passing a band, in which information from the digitalized signals exists, and a phase divider for dividing the components I and Q from the signals which passed through the digital matching filter.

The SAW filter has a pass band, which is designed widely
25 enough to include all VSB signals of the intermediate frequency band.

The carrier wave restoring part includes a pilot extracting part for extracting pilot signals of the components I and Q from the signals of the digitalized pass band, a multiplier for multiplying a complex carrier wave to the extracted pilot signals I and Q to convert them to a base band, a frequency/phase error detecting part for detecting frequency and phase errors from the pilot signals I and Q of the base band, a loop filter for converting the frequency and phase errors to DC components by filtering, and a numerical control oscillator NCO for generating a complex carrier wave proportional to the DC components of the loop filter to output to the multiplier and the demodulator.

The pilot extracting part modulates an IIR low band pass filter of a lower degree to sine waves and cosine waves.

The frequency/phase error detecting part includes a code detector for detecting codes of the pilot signal I which is output from the multiplier, a delay for delaying the detected code components for N sampling, and a multiplier for multiplying an output from the delay with the pilot signal Q, which is output from the multiplier, to output to the loop filter.

An apparatus for restoring carrier waves of a VSB receiver according to the present invention, includes a pilot extracting part for extracting pilot signals of components I and Q from signals of a digitalized pass band, a multiplier for multiplying a complex carrier wave to the extracted pilot

signals I and Q to convert them to a base band, a frequency/phase error detecting part for detecting frequency and phase errors from the pilot signals I and Q of the base band, a loop filter for converting the frequency and phase errors to a DC component by filtering, and a numerical control oscillator for generating a complex carrier wave proportional to the DC components of the loop filter to output to the multiplier and the demodulator.

A carrier wave restoring method of a VSB receiver according to the present invention includes the steps of (a) extracting pilot signals of components I and Q from signals of a digitalized pass band, (b) multiplying a complex carrier wave to the extracted pilot signals I and Q to convert them to a base band, (c) detecting frequency and phase errors from the pilot signals I and Q of the base band, (d) converting the frequency and phase errors to a DC component by filtering, and (e) generating a complex carrier wave proportional to the DC components to output to step (b).

In step (a), an IIR low band pass filter of a lower degree is modulated to sine waves and cosine waves to extract the pilot signals of the components I and Q.

According to the present invention, carrier wave restoring may be carried out digitally without losing the symmetry even when an output spectrum of a tuner has a large offset in a random direction from a preset carrier wave frequency.

Additional advantages, objects, and features of the invention will be set forth in part in the description which follows and in part will become apparent to those having ordinary skill in the art upon examination of the following or
5 may be learned from practice of the invention. The objects and advantages of the invention may be realised and attained as particularly pointed out in the appended claims.

BRIEF DESCRIPTION OF THE DRAWINGS

10 The invention will be described in detail with reference to the following drawings in which like reference numerals refer to like elements wherein:

15 Fig. 1 is a block diagram showing a structure of a related art vestigial sideband transmission device;

20 Fig. 2a is a view showing a spectrum of a DC inserted vestigial sideband transmission signal of a base band;

25 Fig. 2b is a view showing a spectrum of the signal, which is converted to a transmission signal of the vestigial sideband;

Fig. 3 is a block diagram showing a structure of a related art vestigial sideband receiver with an analogue FPLL;

Fig. 4a is a view showing an example of an output of a saw filter in case that a positive frequency offset exists;

Fig. 4b is a view showing an example of an output of a saw filter in case that a negative frequency offset exists;

Fig. 5 is a block diagram of a related art vestigial

sideband receiver in which a carrier wave is restored digitally;

Fig. 6 is a block diagram of a vestigial sideband receiver according to a preferred embodiment of the present invention;

Fig. 7 is a view showing operations of a digital frequency/phase error detector in case that a frequency error exists; wherein

Fig. 7(a) is a view showing an example of component I which is output from a second multiplier;

Fig. 7(b) is a view showing an example of component Q of the second multiplier;

Fig. 7(c) is a view showing an example of an output of a code detector of Fig. 6.

Fig. 7(d) is a view showing an example of an output of a delay of Fig. 6;

Fig. 7(e) is a view showing an example of an output of a third multiplier of Fig. 6;

Fig. 8 is a view showing operations of the digital frequency/phase error detector in case that a phase error exists, wherein

Fig. 8(a) is a view showing an example of component I which is output from the second multiplier of Fig. 6;

Fig. 8(b) is a view showing an example of component Q which is output from the second multiplier of Fig. 6; and

Fig. 9 is a block diagram showing an example of a complex

carrier wave band pass filter of Fig. 6.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Now, preferred embodiments of the present invention will
5 be described in detail with reference to the accompanying drawings.

Fig. 6 is a block diagram showing a structure of a vestigial sideband VSB receiver in a whole digital system according to the present invention, which includes a tuner 601 for selecting a desired channel frequency via an antenna and converting a VSB signal of a RF band carried on the channel frequency to an IF band, a surface acoustic wave SAW filter 602 for passing a predetermined band of an intermediate frequency output from the tuner 601, an A/D converter 603 for digitalizing an output from the SAW filter 602, a re-sampling part 604 for compensating timing errors of current symbols, which are restored via a signal processing of the base band and received from a timing restoring part 605, in a direction for reducing errors between the digitalized signals, a carrier wave restoring part 610 for extracting a complex pilot component from an output of the re-sampling part 604 and restoring carrier waves by using the extracted complex pilot components, a digital matching filter 606 for passing a band in which information from the output from the re-sampling part
20 604, a phase divider 607 for separating components I and Q from the matching filter 606, a first multiplier 608 for

multiplying an output from the phase divider 607 with the restored carrier wave to demodulate the output to signals I and Q of the base band, and an I channel processing part 609 for restoring a real data by receiving the demodulated signal I of the base band from the multiplier 608.

The carrier wave restoring part 610 includes I and Q band pass filters 611 and 612 for extracting outputs from the re-sampling part 604, that is, complex pilot components from the pass band, a second multiplier 613 for converting the carrier pilot components from the pass band to the base band, a frequency/phase error detecting part 615 for detecting frequency and phase errors from the pilot signals I and Q of the base band, a loop filter 619 for filtering the detected frequency and phase errors, and a numerical control oscillator NCO 614 for generating a complex carrier wave proportional to an output from the loop filter 619 to output to a first multiplier 608 and the second multiplier 613.

The frequency/phase error detecting part 615 includes a code detector 616 for detecting codes of the pilot signal I which is output from the second multiplier 613, a delay 617 for delaying the detected code components for a predetermined time period, and a third multiplier 618 for multiplying an output from the delay 617 with the pilot signal Q to output to the loop filter 619.

In the present invention as described above, as a VSB modulated RF signal is received via an antenna, a desired

channel frequency is selected and a VSB signal of the RF band, which is carried on the channel frequency, is lowered to a fixed IF band (IF; in general, 44MHz or 43.75MHz is widely used) to properly filter signals of other channels.

5 The output from the tuner 601 is removed its residual signals of the other channels and noise components while passing the SAW filter 602, wherein the pass band of the SAW filter 602 is designed widely enough to include the VSB signals of the IF band. That is, the SAW filter 602 does not carry out any matching functions but removes noise and the other channels' signals, wherein the SAW filter 602 employs an element having a wide pass band.

10 The IF signals, which are passed through the SAW filter 602, are input to the A/D converter 603, and the A/D converter 603 directly digitalizes the IF signals to a fixed frequency to output to the re-sampling part 604. The re-sampling part 604 compensates timing errors of current symbols, which are restored via a signal processing of the base band and received from the timing restoring part 605, in a direction for
15 reducing errors between the digitalized signals, and outputs a sample of 21.52MHz, which is twice of a symbol ratio of 10.76MHz.

20 On the other hand, in the present invention, it is possible to use a structure that an analogue signal, which
25 passed through the SAW filter 602 instead of the A/D converter 603 and the re-sampling part 604, is lowered to a secondary

intermediate frequency (2nd IF, in general, 5.38MHz is employed) by an analogue mixer and a digital data of 21.52MHz is directly obtained by an A/D converter which uses VCXO as an input clock.

5 In the present invention, the operation may be carried out regardless of either selection, so that the structure adopting a re-sampler as shown in Fig. 6 will be described as an example.

20 An IF data, which is sampled to twice of the symbol ratio, becomes a sample having a maximum SNR while passing the digital matching filter 606. The signals which are passed the digital matching filter 606 are divided into components I and Q in the phase divider 607 for the complex demodulation and then output to the first multiplier 608. The first multiplier 608 multiplies the signals I and Q of the base band respectively with a complex carrier wave of the NCO 614, which are restored in the carrier wave restoring part 610, in order to demodulate them to the signals I and Q of the base band.

25 At this time, the signal I of the base band is input to the I channel processing part 609 for the data restoring, and the signal Q of the base band is not used, so that the first multiplier 608 as shown in Fig. 6 may be composed with not a complex multiplier but two multipliers and one adder for extracting real number components. In the meantime, the complex multiplier is composed with four multipliers and two adders for lowering real number components and imaginary

number components respectively to the base band.

The I channel processing part 609 restores data by performing a process such as a synchronising signal extraction, a channel compensation, and error correction with relation to the input signal I of the base band.

On the other hand, as described hereinabove, the pilot signals are included in the output signal of the re-sampling part 605 even when the frequency offset exists, since the SAW filter 602 has a wide pass band for including all VSB transmission signal band widths. Accordingly, the pilot signal may be directly extracted from the pass band by using the complex band pass filters 611 and 612 of the carrier wave restoring part 610.

The I and Q band pass filters 611 and 612 may be realised by coefficients which modulates a finite impulse response FIR low pass filter with a sine wave and a cosine wave, which requires a lot of hardware for realising the band pass filter. Therefore, it is simple in view of the hardware to modulate an infinite impulse response IIR low pass filter of a simple degree with the sine wave and the cosine wave, in which the pilot signal may be precisely extracted with a smaller degree. As a realisation of a simple primary low pass filter will be described with reference to examples of All-pole IIR filter and Butterworth IIR filter in detail hereinafter.

The complex pilot signals, which are extracted from the pass band via the I and Q band pass filters 611 and 612, are

input to the second multiplier 613 and lowered to the base band by being multiplied with the complex carrier wave of the NCO 614, which is used for the lowering of the VSB signals of the pass band to the base band. If it is assumed that the pilot signal is P, an In-phase component I thereof is Pr, and a Quadrature-phase component Q thereof is Pi, then $P = Pr + jPi$.

If the frequency and phase offset is 0, and the noise and VSB signal components introduced together are ignored, the signal Pr is represented by a DC signal (for example, $\cos 0^\circ = 1$), and the signal Pi is represented by 0 (for example, $\sin 0^\circ = 0$). Therefore, if the pilot signal component I of the base band, which is represented by DC, is passed through the code detector 616 and the delay 617, and multiplied with the pilot signal component Q of the base band by the third multiplier 618, the output from the third multiplier 618 becomes 0, which has no effect on the loop filter 619.

On the other hand, if the frequency offset is f_0 , then, the signals Pr and Pi are represented by following formula 1.

[formula 1]

$$p_r = \cos\left(\frac{2\pi f_0}{f_s} n\right)$$

$$p_i = \sin\left(\frac{2\pi f_0}{f_s} n\right)$$

wherein, f_s is a sampling frequency, which is twice of the symbol ratio, that is, 21.52MHz. Therefore, the signals P_r and P_i are shown by the sine wave and the cosine wave respectively having a period of $1/f_0$ as shown in Fig. 7(a) and Fig. 7(b).

5 The component I of Fig. 7(a) is input to the code detector 616 of the frequency/phase error detector 615 for using the code values only, as shown in Fig. 7(c). The code values, which are output from the code detector 616, are delayed for N sampling as shown in Fig. 7(d) and output to the third multiplier 618.

10 If the signal, which is delayed by N sampling in the third multiplier 618, is multiplied with the cosine wave of the component Q as shown in Fig. 7(b), a DC component which is proportional to a frequency error exists as shown in Fig. 7(e). That is, the frequency/phase error detector 615 carries out the frequency error detection. While an output from the frequency/phase error detector 615 passes through the loop filter 619, the DC component is extracted and input to the NCO 614. The NCO 614 raises or lowers a current operation frequency according to the DC component and outputs to the
20 first and second multipliers 608 and 613.

The value N that determines how many samples to be delayed in the delay 617 is an important parameter for determining a pull in range of the frequency error detector. For example, if the value N is small, the error is generated
25 over a wide range frequency, while the DC value of the error becomes decreased, decreasing a gain of the detector. To the

contrary, if the value N is large, the operation range is small while the DC value of the error becomes increased, increasing the gain of the detector, thereby reducing the phase noise and making it possible to restore the carrier waves.

Therefore, the value N should be selected carefully. In general, in order to obtain carrier waves of $-200\text{KHz} \sim +200\text{KHz}$, $N=50 \sim 75$.

If the frequency offset becomes 0 while repeating the above procedure, the signal P_r of the pilot signal component I of the base band is represented by DC.

If it is assumed that the frequency offset is 0 and a phase error exists, that is, the signal P_r is DC as shown in Fig. 8(a) and the signal P_i is a cosine wave which is not 0 as shown in Fig. 8(b), the signal Q and the code values of the DC, that is, I or $-I$ are multiplied through all time periods in the third multiplier 618. It may be seemed to have no frequency locked-loop FLL.

Therefore, the third multiplier 618 outputs only a phase error value of the cosine wave with relation to the phase error to the loop filter 619 and the loop filter 619 extracts the DC component to output to the NCO 614. The NCO 614 amends the phase error by raising or lowering the current operation frequency and outputs to the first and second multipliers 608 and 613.

Consequently, in the frequency/phase error detector 615, the FLL initiatively operates for making the frequency error 0 and then the PLL initiatively operates for amending the phase as the frequency is restored.

5 Now, the complex band pass filters 611 and 612 that extract the pilot signals from the pass band will be described in more detail. The complex band pass filters 611 and 612 modulate the IIR low pass filter of a lower degree to the sine and cosine waves as described above in order to obtain the complex pilot signals.

As an example of the IIR low pass filter of a lower degree, a primary All Pole low pass filter may be used. For example, Z-conversion of the primary All Pole low pass filter is referred to $H(z)$, $H(z)$ may be represented by formula 2.

[formula 2]

$$H(z) = s \cdot \frac{1}{1 - az^{-1}}$$

wherein, s is a normalisation constant for making a DC gain 1, and a is a value for determining a 3-dB band area, which may be 0~1.

20 If the sine and cosine modulation signals of $H(z)$ of formula 2 are respectively referred to $H_r(z)$ and $H_i(z)$, $H_r(z)$ and $H_i(z)$ may be represented by formula 3.

[formula 3]

$$H_r(z) = s \cdot \frac{1 - a \cdot \cos \omega_c z^{-1}}{1 - a \cdot \cos \omega_c z^{-1} + z^{-2}}$$

$$H_i(z) = s \cdot \frac{1 - a \cdot \sin \omega_c z^{-1}}{1 - a \cdot \cos \omega_c z^{-1} + z^{-2}}$$

wherein, ω_c is a normalised carrier wave frequency, which is represented by formula 4.

[formula 4]

$$\omega_c = 2\pi \frac{f_c}{f_s} \quad (f_c \text{ is an analog carrier wave frequency and } f_s$$

is a sampling frequency)

In $H_r(z)$ and $H_i(z)$ of formula 3, the denominators are equal to each other and numerators are different from each other. Therefore, the complex band pass filters 611 and 612 use a transposed IIR structure and shares a common denominator as shown in a structure of Fig. 9. In Fig. 9, by sharing a common denominator, formula 3 is realised in hardware as it is, wherein a multiplier 801, an adder 802, delays 804 and 805, a multiplier 808 and a subtracter 809 correspond to the denominator of formula 3.

On the other hand, as another example of the complex band pass filters 611 and 612, a low pass filter of a primary Butterworth may be utilised, wherein if it is assumed that z conversion of the low pass filter of the primary Butterworth

is $B(z)$, $B(z)$ may be represented by following formula 5.

[formula 5]

$$B(z) = s \cdot \frac{1 - z^{-1}}{1 - az^{-1}}$$

wherein, s and a have the same meaning with the above
5 formula 3.

If the sine and cosine modulation signals of $B(z)$ are $B_r(z)$ and $B_i(z)$, $B_r(z)$ and $B_i(z)$ are represented by following formula 6.

[formula 6]

$$B_r(z) = s \cdot \frac{1 - a \cdot \cos \omega_c z^{-1}}{1 - a \cdot \cos \omega_c z^{-1} + z^{-2}}$$

$$B_i(z) = s \cdot \frac{1 - a \cdot \sin \omega_c z^{-1}}{1 - a \cdot \cos \omega_c z^{-1} + z^{-2}}$$

wherein, ω_c is a normalised carrier wave frequency and represented by formula 7.

[formula 7]

15
$$\omega_c = 2\pi \frac{f_c}{f_s}$$

Similarly, $B_r(z)$ and $B_i(z)$ have a same denominator so that the complex band pass filters 611 and 612 having the similar structure with Fig. 9 may be realised, in which a common

denominator is shared by $B_r(z)$ and $B_i(z)$ and the numerators are different from each other.

As described hereinabove, according to the VSB receiver and the carrier wave restoring method of the present invention, the extraction of the pilot signals from the pass band and the restoring of the carrier waves are performed digitally, so that it is not necessary to attach an analogue element such as the VCO to an outside additionally. Further, the frequency/phase error detector detects symmetrical errors with relation to the carrier wave frequency offset, are restored, so that carrier waves may be stably obtained and pursued even with relation to the both positive and negative frequency offsets.

Furthermore, even though a complex multiplier is added and the complex low filter, which was necessary to extract the pilot signals from the base band, is removed to the pass band filter, the present invention requires no more hardware differently from the previous manner in which complicated hardware should be added. Owing to the added complex multiplier, the complex multipliers, that is four real number multipliers and two adders which are required in the related art for lowering the VSB signals of the pass band to the base band may be simplified to two multipliers for extracting real number components and one adder. Therefore, if the complex pass band filter is realised, there comes an effect to reduce the hardware, since the multipliers and the adders may be

saved in comparison with the case to realise them separately.

The foregoing embodiments and advantages are merely exemplary and are not to be construed as limiting the present invention. The present teaching can be readily applied to
5 other types of apparatuses. The description of the present invention is intended to be illustrative, and not to limit the scope of the claims. Many alternatives, modifications, and variations will be apparent to those skilled in the art. In the claims, means-plus-function clauses are intended to cover the structures described herein as performing the recited function and not only structural equivalents but also equivalent structures.

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